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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/666,484	09/19/2003	Subhas Bothra	PHA51101B	8867

24738 7590 07/21/2004

PHILIPS ELECTRONICS NORTH AMERICA CORPORATION
INTELLECTUAL PROPERTY & STANDARDS
1109 MCKAY DRIVE, M/S-41SJ
SAN JOSE, CA 95131

EXAMINER

STEIN, STEPHEN J

ART UNIT

PAPER NUMBER

1775

DATE MAILED: 07/21/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/666,484	BOTHRA ET AL.	
	Examiner Stephen J Stein	Art Unit 1775	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on ____.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 40-43 is/are pending in the application.
 - 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) Claim(s) ____ is/are allowed.
- 6) Claim(s) 40-43 is/are rejected.
- 7) Claim(s) ____ is/are objected to.
- 8) Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on ____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. ____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. ____ . |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: ____ . |

DETAILED ACTION

Priority

1. Applicant has not complied with one or more conditions for receiving the benefit of an earlier filing date under 35 U.S.C. 120 as follows:

An application in which the benefits of an earlier application are desired must contain a specific reference to the prior application(s) in the first sentence of the specification or in an application data sheet (37 CFR 1.78(a)(2) and (a)(5)). The specific reference to any prior nonprovisional application must include the relationship (i.e., continuation, divisional, or continuation-in-part) between the applications except when the reference is to a prior application of a CPA assigned the same application number.

Specification

2. The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 CFR 1.75(d)(1) and MPEP § 608.01(o). Correction of the following is required: The limitation “conductive line” lacks antecedent basis in the specification.

Claim Objections

3. The claims are objected to since there is a duplicate claim 43.

Claim Rejections - 35 USC § 102

4. Claims 40, 41 and 43 are rejected under 35 U.S.C. 102(b) as being anticipated by US Patent US 5,792,707 (Chung).

Chung teaches a method for global planarization of integrated circuit structures using CMP (chemical mechanical polishing) with a polish stop layer (antireflective layer) (col. 1, lines

6-10). Chung further teaches that the article to be polished includes a semiconductor substrate (10), a conductive metal material patterned to form conductive lines (14) (underlying patterned conductive line) on the substrate, a polish stop layer preferably composed of silicon nitride (26) (non-light transmissive anti-reflective material), and finally, a dielectric layer of silicon oxide (amorphous silica) or TEOS (30) (light transmissive dielectric layer) which is shown to be substantially aligned with and disposed above the underlying conductive lines (See Figures 1-5). Chung further teaches that the term substrate is understood to possibly include a semiconductor wafer (col. 3, lines 10-15).

Claim Rejections - 35 USC § 103

5. Claim 42 is rejected under 35 U.S.C. 103(a) as being obvious over Chung and further in view of US Patent 6,013,574 (Hause).

As stated above, Chung teaches an integrated circuit with a patterned anti-reflective polish stop layer over components, but does not specifically teach that the reflectance polish stop layer is amorphous silicon or silicon oxynitride.

Hause teaches an interconnect structure with an anti-reflective material on top of the conductive material. Hause further teaches that common anti-reflective materials include silicon oxynitride (SiON) and amorphous silicon (col. 2, lines 37-39). Consequently, it would have been obvious to one of ordinary skill in the art at the time of the invention to substitute SiON or amorphous silicon for the anti-reflective layer, because these materials are commonly known anti-reflective materials suitable for the same purpose.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stephen Stein whose telephone number is 572-272-1544. The examiner can normally be reached on Monday through Friday from 8:30 a.m. to 5:00 p.m. If the attempts to reach the examiner are unsuccessful, the examiner's supervisor, Deborah Jones can be reached by dialing 571-272-1535. The official fax number is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

July 16, 2004


Stephen J. Stein
Primary Examiner
Art Unit 1775